

REMARKS

Claims 1, 3 and 8 are active. Claims 1, 3 and 8 are rejected under 35 USC 103 as being unpatentable over Clemen in view of Mutsaers. The drawing and specification previously objected to based on the prior rejection of claim 1, now withdrawn, are acceptable.

Amended claims 1, 3 and 8 are submitted for the Examiner's reconsideration.

Amended claim 1 is not suggested, disclosed or otherwise made obvious by any of the cited references of record including Clemen and/or Mutsaers, taken individually or in combination.

Amended claim 1 calls for:

the drain-source electrodes of the charging and switching transistors being arranged to be coupled in series between a voltage source and a reference potential such that the gate electrode of the charging FET is not connected via an electrical line directly to a voltage source, to the reference potential, to the input or to the output; wherein the gate electrode of the charging FET is directly capacitively coupled to one of the source/drain electrodes of the charging FET to thereby provide a potential at the gate electrode of the charging FET solely via the capacitive coupling (underlining added)

Clemen is cited as disclosing this structure (without the amended material) except the FET transistor is not organic and is missing the claimed structure. This is not true. The Action states that this reference discloses the gate electrode of the charging FET is not connected via an electrical line directly to **THE** (emphasis added) voltage source, to the reference potential, to the input, or to the output wherein the gate electrode of the charging FET is directly capacitively coupled to one of the source/drain electrodes of the charging FET.

This conclusion is in error. The claim does not call for

“the gate electrode of the charging FET is not connected via an electrical line directly to the voltage source” as asserted (underlining added)

The claim calls for

“the gate electrode of the charging FET is not connected via an electrical line directly to a voltage source (emphasis added)

The term “a voltage source” means any voltage which can be deemed a source rather than a specific voltage that normally would be deemed a voltage source in a conventional definition of “voltage source” such as a power supply, a battery etc. Clemen discloses that transistors 10 and 7 have an output at node 23. This node is connected to those outputs by an electrical line as is plain from this reference. The voltages from transistors 7 and 10 comprises a source of voltage connected to the gate electrode by an electrical line. This is contrary to the prior and present amended claim 1, not suggested by this reference. The capacitive coupling (not the same as an electrical line as claimed) is not the same as a direct coupling of a voltage by an electrical line as claimed. Claim 1 is not suggested or disclosed by Clemen. However, claim 1 is further amended as noted by the underlining in this claim above. The amended provision calls for:

to thereby provide a potential at the gate electrode of the charging FET solely via the capacitive coupling (underlining added)

As claimed in amended claim 1, the only potential appearing at the gate electrode of the charging FET is that provided by the capacitive coupling. This is what was intended by the prior language calling for no voltage being applied to the gate electrode via a direct electrical line (not a capacitor as claimed which does not connect a voltage by an electrical line to the gate electrode, but by a different mode, i.e., capacitive coupling).

Plainly, Clemen does not suggest or disclose this structure. This reference does not suggest claim 1.

Mutsaers is cited for disclosing an organic transistor constructed as claimed and is missing the claimed structure discussed above. For the reasons given, amended claim 1 is believed allowable. Claims 3 and 8 depend from claim 1 and are believed allowable for at least the same reasons. Since claims 1, 3 and 8 have been shown to be in proper form for allowance, such action is respectfully requested.

The Commissioner is authorized to respectively charge or credit deposit account 03 0678 for any under or overpayments in connection with this paper as noted on the first page of this paper including the one month extension of time fee of \$130.

Respectfully submitted,
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